

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination SINGH ET AL.	
		Examiner Leigh Marie Garbowski	Art Unit 2825	Page 1 of 1

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NON-PATENT DOCUMENTS

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	V	SINGH et al., "Incremental Placement for Layout-Driven Optimizations on FPGAs," IEEE/ACM 2002 Int'l Conference on CAD, pages 752-759.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.